

AP30H80G

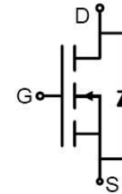
N-Channel Enhancement Mosfet

AIIPOWER

DATA SHEET

Features

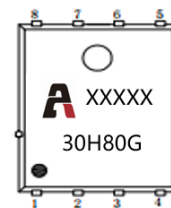
- 30V,90A
 $R_{DS(ON)} < 4.8m\Omega @ V_{GS}=10V$ TYP:4.0m Ω
 $R_{DS(ON)} < 8.5m\Omega @ V_{GS}=4.5V$ TYP:6.8m Ω
- Advanced Trench Technology
- Lead free product is acquired
- Excellent $R_{DS(ON)}$ and Low Gate Charge



Schematic Diagram

Applications

- Power management
- Load Switch
- PWM applications



Marking and pin Assignment

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
30H80G	AP30H80G	PDFN5X6	-	-	5000

ABSOLUTE MAXIMUM RATINGS ($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_c=25^\circ\text{C}$)	I_D	90	A
Continuous Drain Current ($T_c=100^\circ\text{C}$)	I_D	63	A
Pulsed Drain Current ⁽¹⁾	I_{DM}	320	A
Drain Power Dissipation	P_D	65	W
Single Pulsed Avalanche Energy ⁽²⁾	E_{AS}	138	mJ
Thermal Resistance from Junction to Ambient ⁽³⁾	$R_{\theta JA}$	52	$^\circ\text{C}/\text{W}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.9	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	-55~ +150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~ +150	$^\circ\text{C}$

Notes:

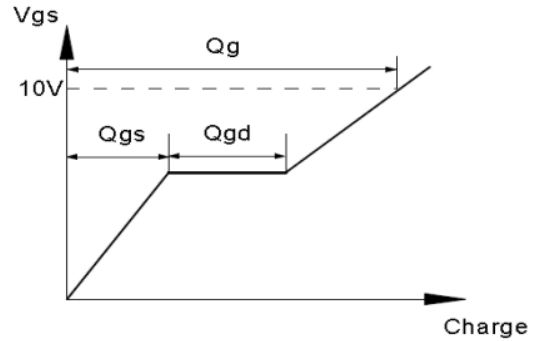
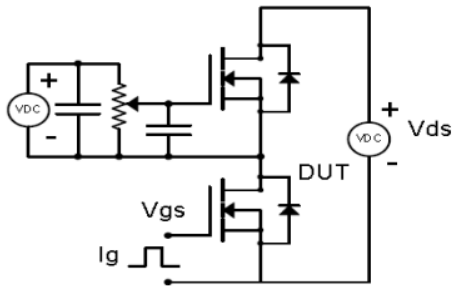
- 1) Repetitive Rating: pulse width limited by maximum junction temperature
- 2) EAS condition : $T_J=25^\circ\text{C}$, $V_{DD}=15\text{V}$, $V_G=10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$, $I_{AS}=23.5\text{A}$
- 3) The value of $R_{\theta JA}$ Mounted on FR4 Board (25.4mm*25.4mm*t1.6mm) With 2oz Copper $T_A=25^\circ\text{C}$

MOSFET ELECTRICAL CHARACTERISTICS($T_J=25^{\circ}\text{C}$ unless otherwise noted)

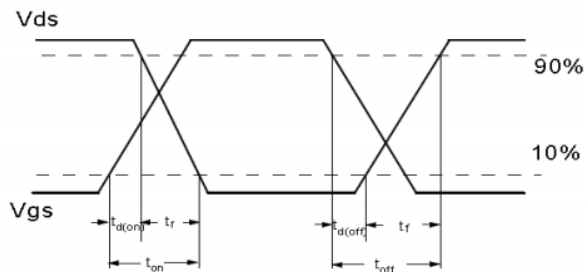
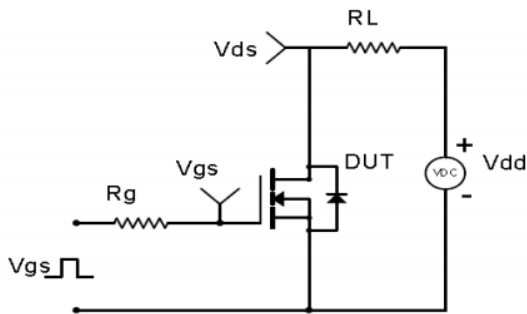
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 30V, V_{GS} = 0V$	-	-	1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	1.5	2.5	V
Drain-source on-resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$	-	4.0	4.8	m Ω
		$V_{GS} = 4.5V, I_D = 15A$	-	6.8	8.5	m Ω
Forward transconductance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$	-	2.6	-	Ω
Dynamic characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 15V, V_{GS} = 0V, f = 1MHz$	-	1897	-	pF
Output Capacitance	C_{oss}		-	244	-	
Reverse Transfer Capacitance	C_{rss}		-	212	-	
Switching characteristics						
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 15V, I_D = 30A, R_G = 3.0\Omega,$ $V_{GS} = 10V$	-	6	-	nS
Turn-on rise time	t_r		-	3.2	-	
Turn-off delay time	$t_{d(off)}$		-	24.2	-	
Turn-off fall time	t_f		-	11.3	-	
Total Gate Charge	Q_g	$V_{DS} = 15V, I_D = 20A,$ $V_{GS} = 10V$	-	37.3	-	nC
Gate-Source Charge	Q_{gs}		-	5.7	-	
Gate-Drain Charge	Q_{gd}		-	9.4	-	
Gate Plateau	$V_{plateau}$		-	3	-	V
Source-Drain Diode characteristics						
Diode Forward voltage	V_{SD}	$T_J = 25^{\circ}\text{C}, V_{GS} = 0V, I_S = 20A$	-	0.8	1.2	V
Diode Forward current	I_S	$T_C = 25^{\circ}\text{C}$	-	-	80	A
Body Diode Reverse Recovery Time	T_{rr}	$T_J = 25^{\circ}\text{C}, V_D = 20V,$ $di/dt = 100A/\mu s, I_F = 20A$	-	16.5	-	nS
Body Diode Reverse Recovery Charge	Q_{rr}		-	6.7	-	nC

Test Circuit & Waveform

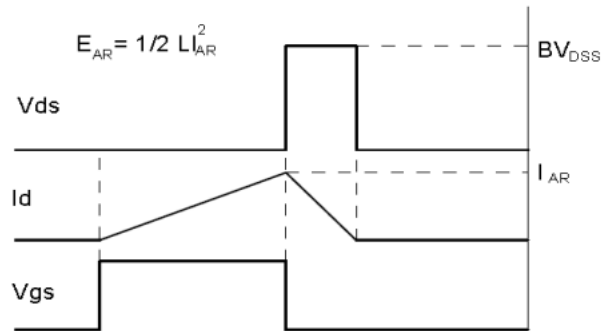
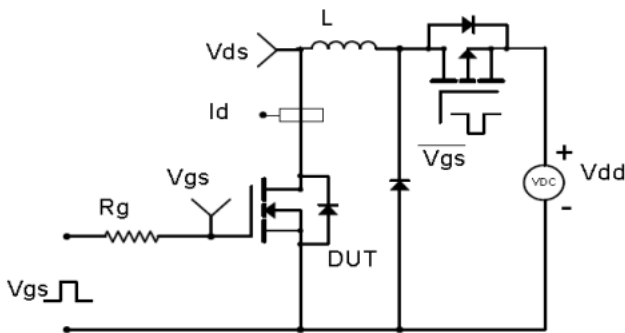
Gate Charge Test Circuit & Waveform



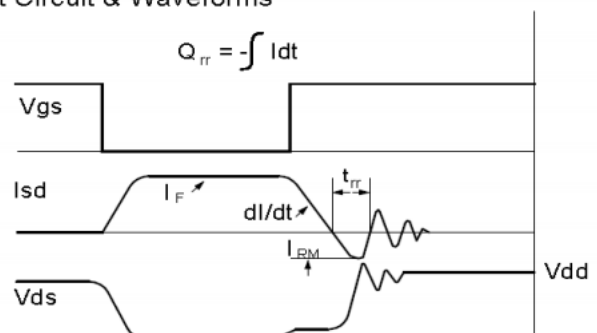
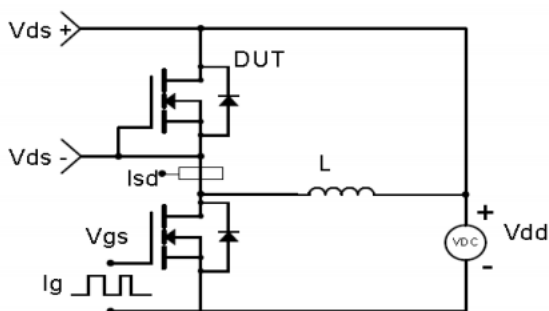
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Typical Characteristics

Figure 1. Output Characteristics

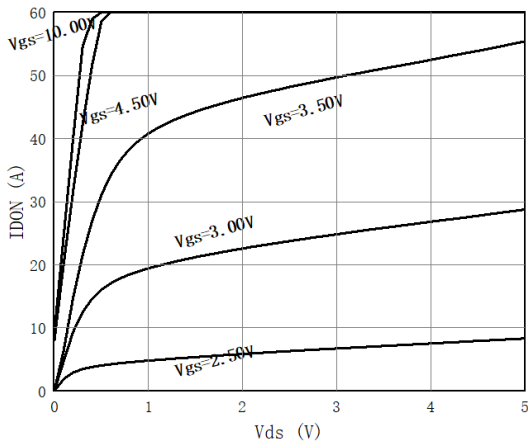


Figure 2. Transfer Characteristics

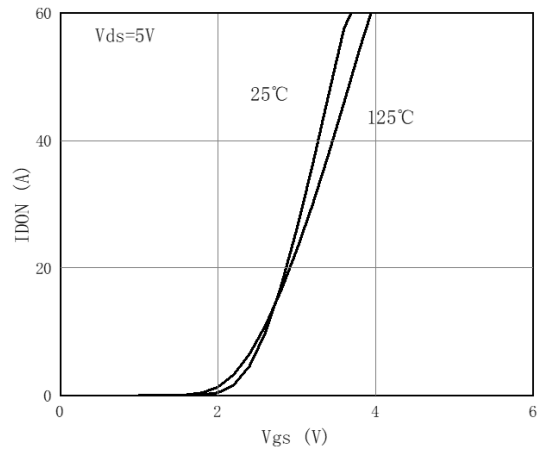


Figure 3. Source Drain Forward Characteristics

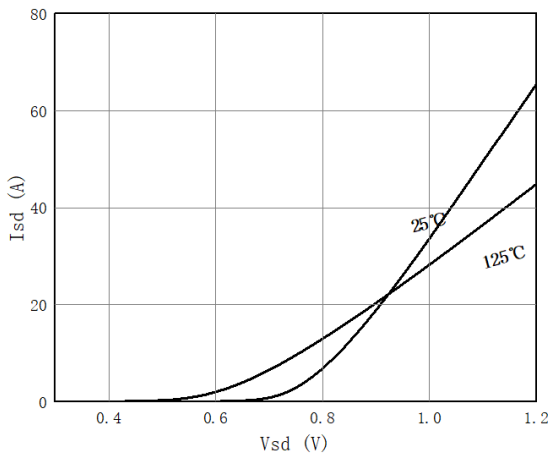


Figure 4. Gate-Charge Characteristics

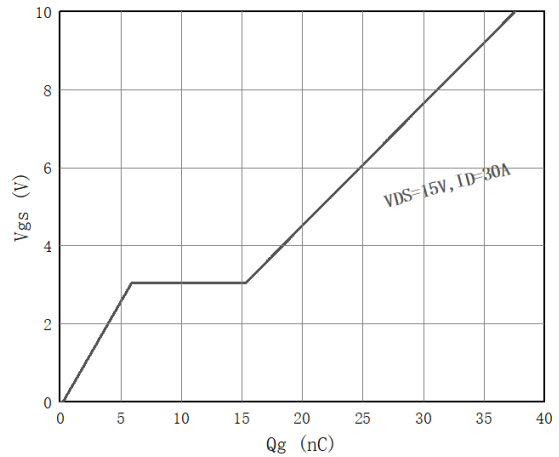


Figure 5. On-Resistance vs. Drain Current and Gate Voltage

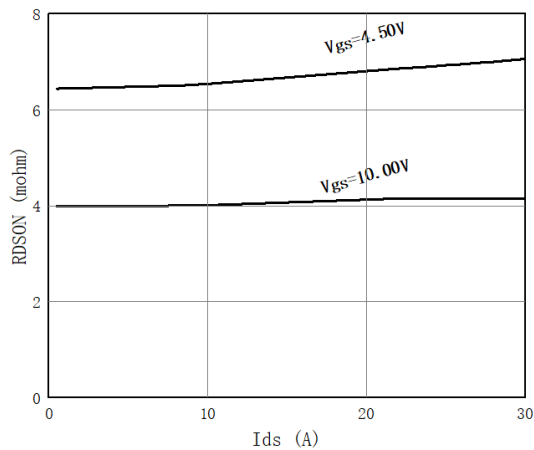


Figure 6. $R_{DS(ON)}$ vs Junction Temperature

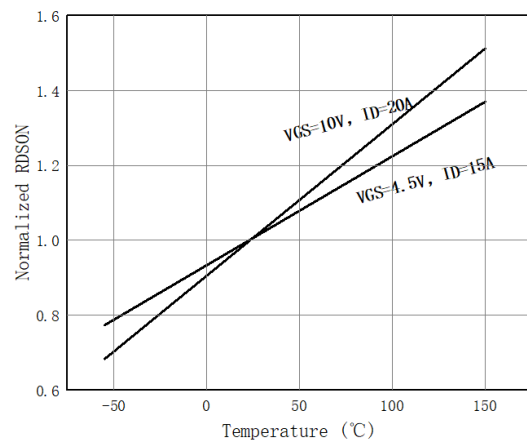


Figure 7: V_{th} vs Junction Temperature

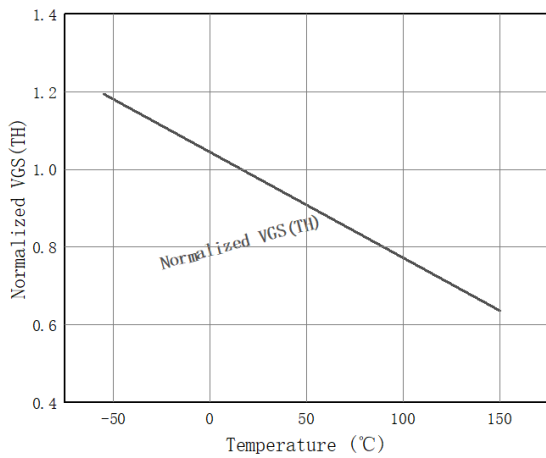


Figure 8: $BVDSS$ vs. Junction Temperature

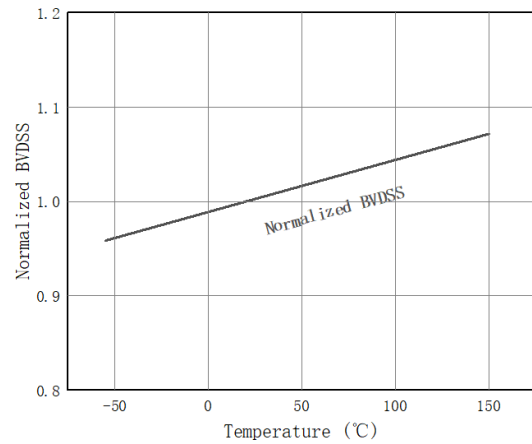


Figure 9. Capacitance

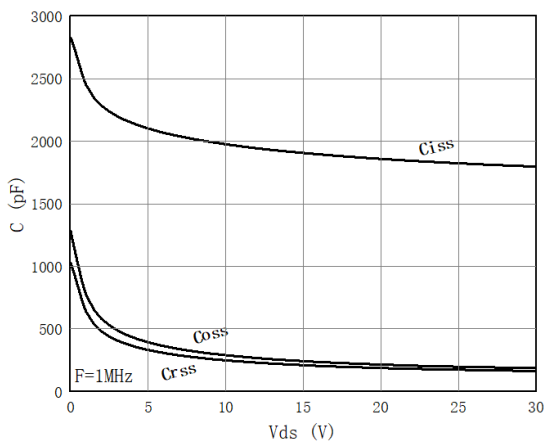


Figure 10. On-Resistance vs. Gate-Source Voltage

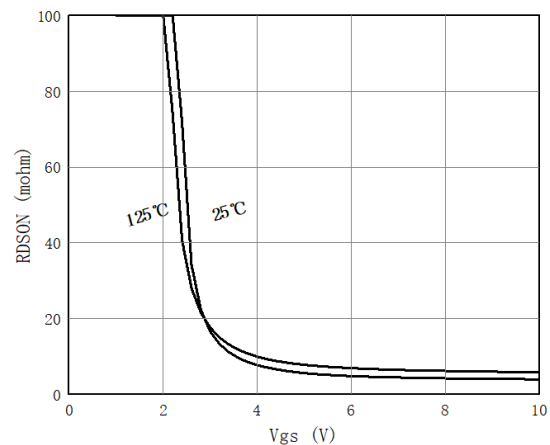


Figure 11. Current De-rating

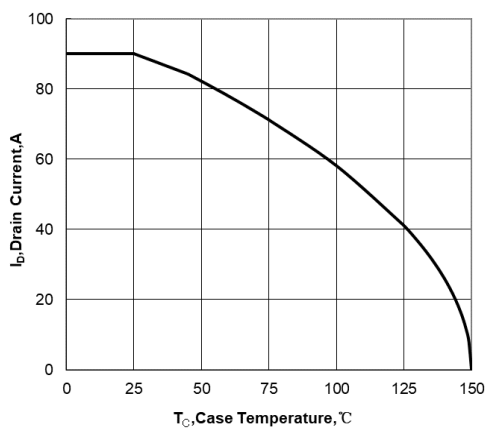


Figure 12. Power De-rating

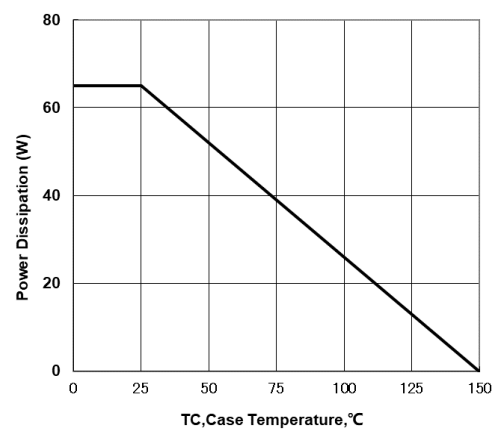


Figure 13. Safe Operating Area

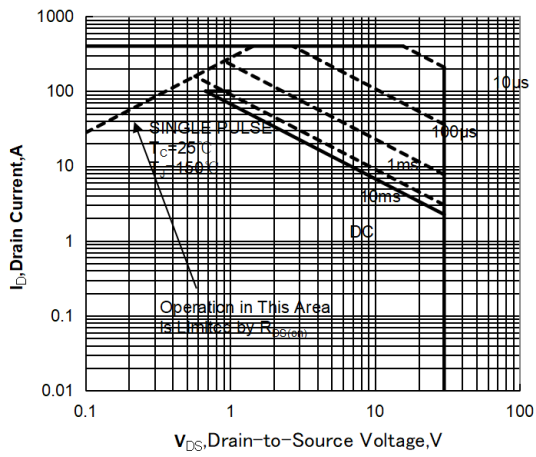
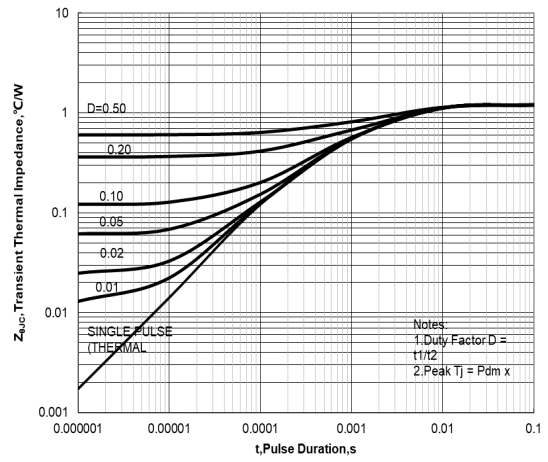
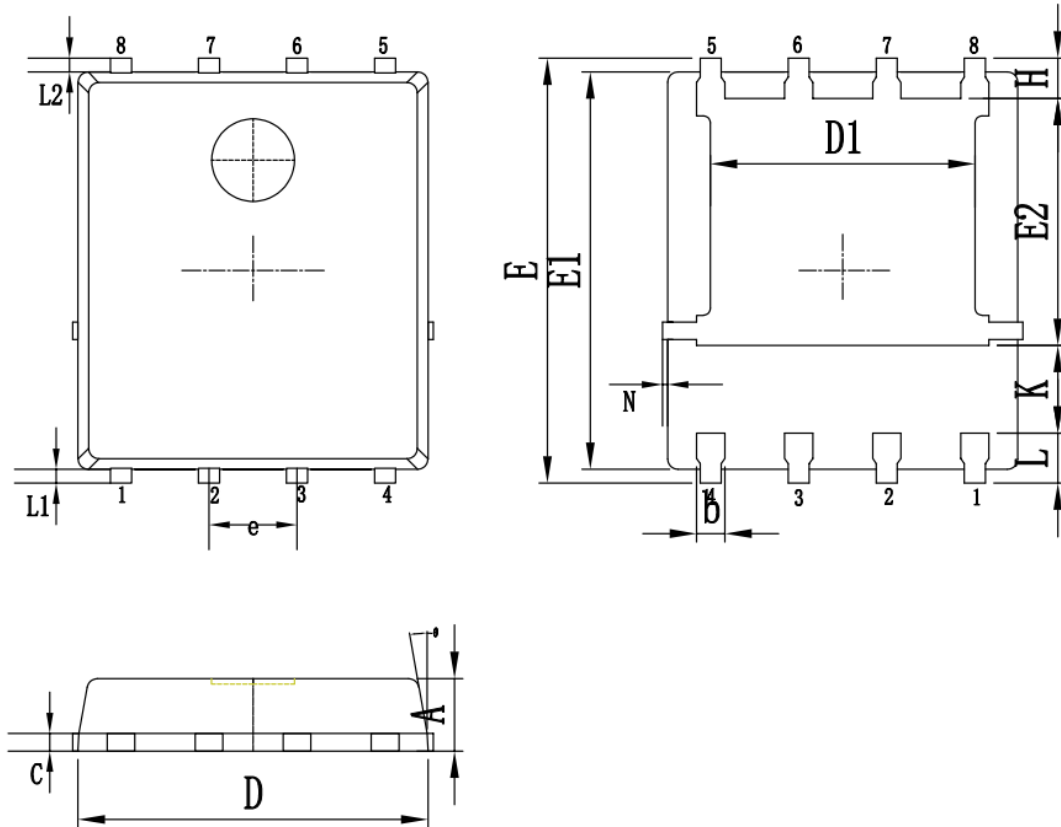


Figure 14. Normalized Maximum Transient Thermal Impedance



PDFN5X6 Package Information



Symbols	Millimeters		
	MIN.	NOM.	MAX.
A	0.90	1.05	1.20
b	0.20	0.40	0.50
C	0.20	0.25	0.35
D	4.80	5.05	5.20
D1	3.80	3.90	4.10
E	5.90	6.00	6.20
E1	5.60	5.75	5.90
E2	3.40	3.50	3.60
e	1.27 BSC.		
H	0.40	0.60	0.70
K	1.17	1.27	1.37
L	0.50	0.74	0.84
L1/L2	0.10	0.16	0.20
θ	8°	10°	12°
N	0	-	0.15

Revision History

Revision	Release	Remark
V1.0	2022/04/02	Initial Release
V1.1	2024/08/10	Update POD

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Allpower assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.